

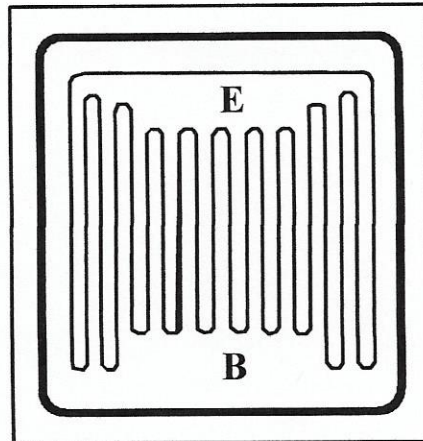


# Sierra Components, Inc.

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Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.



**Top Material: Aluminum**  
**Backside Material: Al/Ti/Ni/Au**  
**Bond Pad Size: .052 x .012"**  
**Backside Potential: Collector**  
**Mask Ref: 0266**

**APPROVED BY: DK**

**DIE SIZE .120" Square**

**DATE: 5/10/17**

**MFG: Microsemi**

**THICKNESS .014"**

**P/N: 2N5154**